

		ISSUE CLASSIFICATION
Class	Subclass	

PATENT NUMBER

**U.S. UTILITY** Patent Application

O.I.P.E. SCANNED <i>CH</i> Q.A. <i>KT</i>	PATENT DATE
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APPLICATION NO. 09/871192	CONT/PRIOR	CLASS 303 439	SUBCLASS -78	ART. UNIT 3613 2839	EXAMINER LE, T.
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## APPLICANTS

**TITLE**

Mark Haller

High integration electronic assembly and method

PTO-2040  
12/99

**ISSUING CLASSIFICATION**

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	<b>Sheets Drwg.</b>	<b>Figs. Drwg.</b>	<b>Print Fig.</b>	<b>Total Claims</b>	<b>Print Claim for O.G.</b>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)  _____ (Primary Examiner) _____ (Date)  _____ (Legal Instruments Examiner) _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
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<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				<b>Amount Due</b>	<b>Date Paid</b>
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